

FIG. 1

FIRST PRINCIPLE DIAGRAM OF PRESENT INVENTION

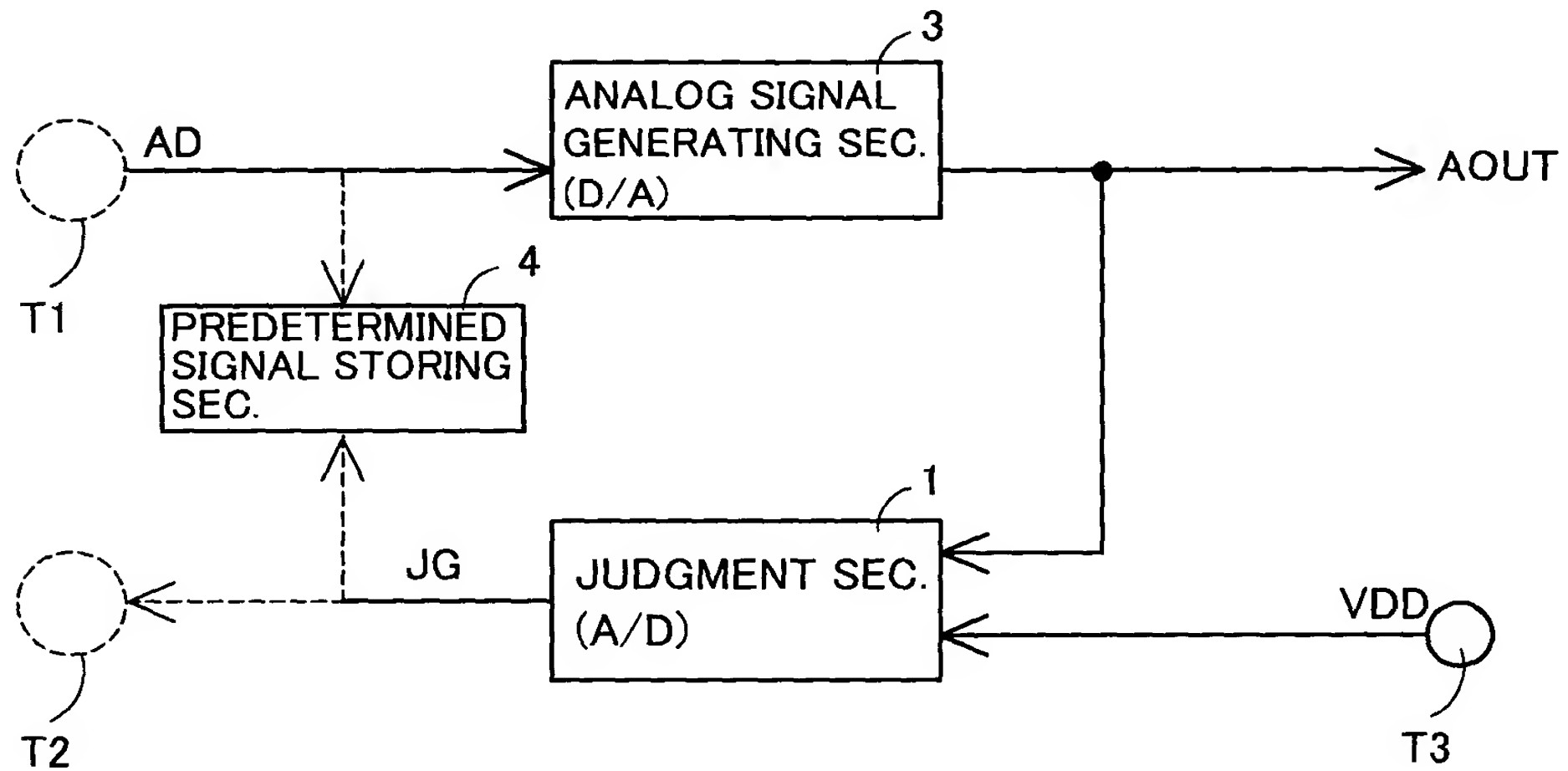


FIG. 2

SECOND PRINCIPLE DIAGRAM OF PRESENT INVENTION

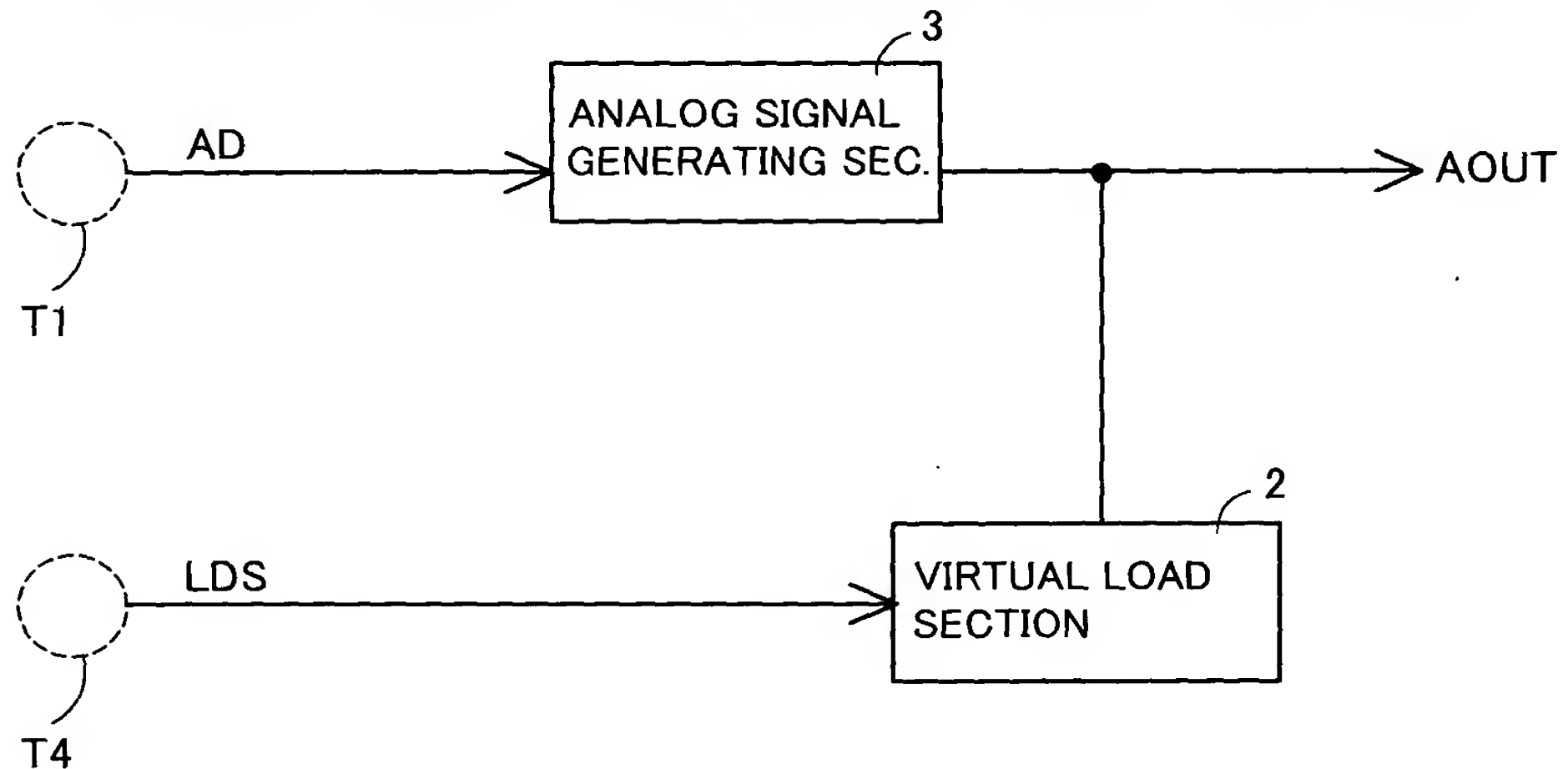


FIG. 3 CIRCUIT BLOCK DIAGRAM OF FIRST EMBODIMENT

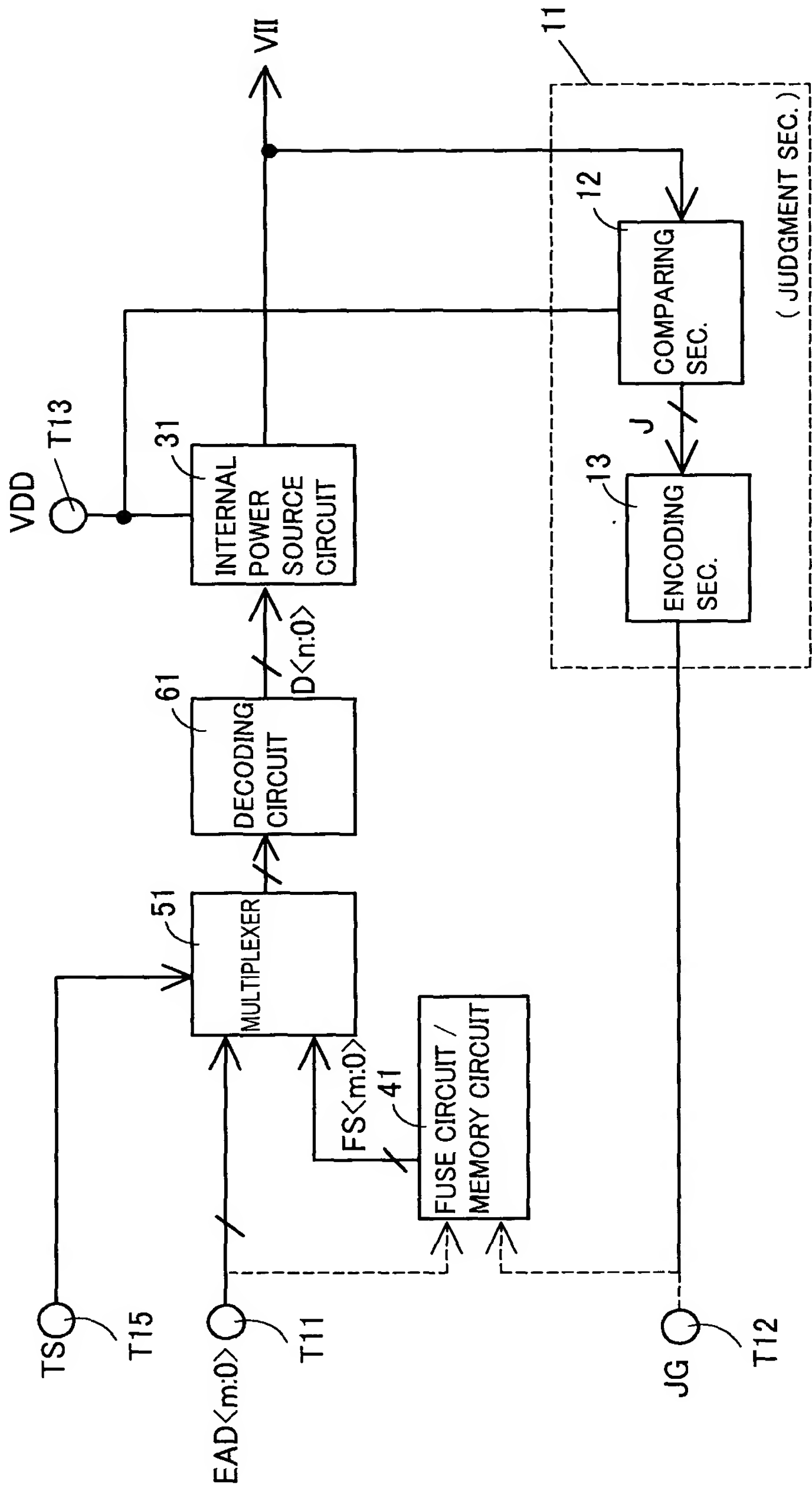


FIG. 4

CIRCUIT DIAGRAM OF SPECIFIC EXAMPLE DIRECTED TO FIRST EMBODIMENT

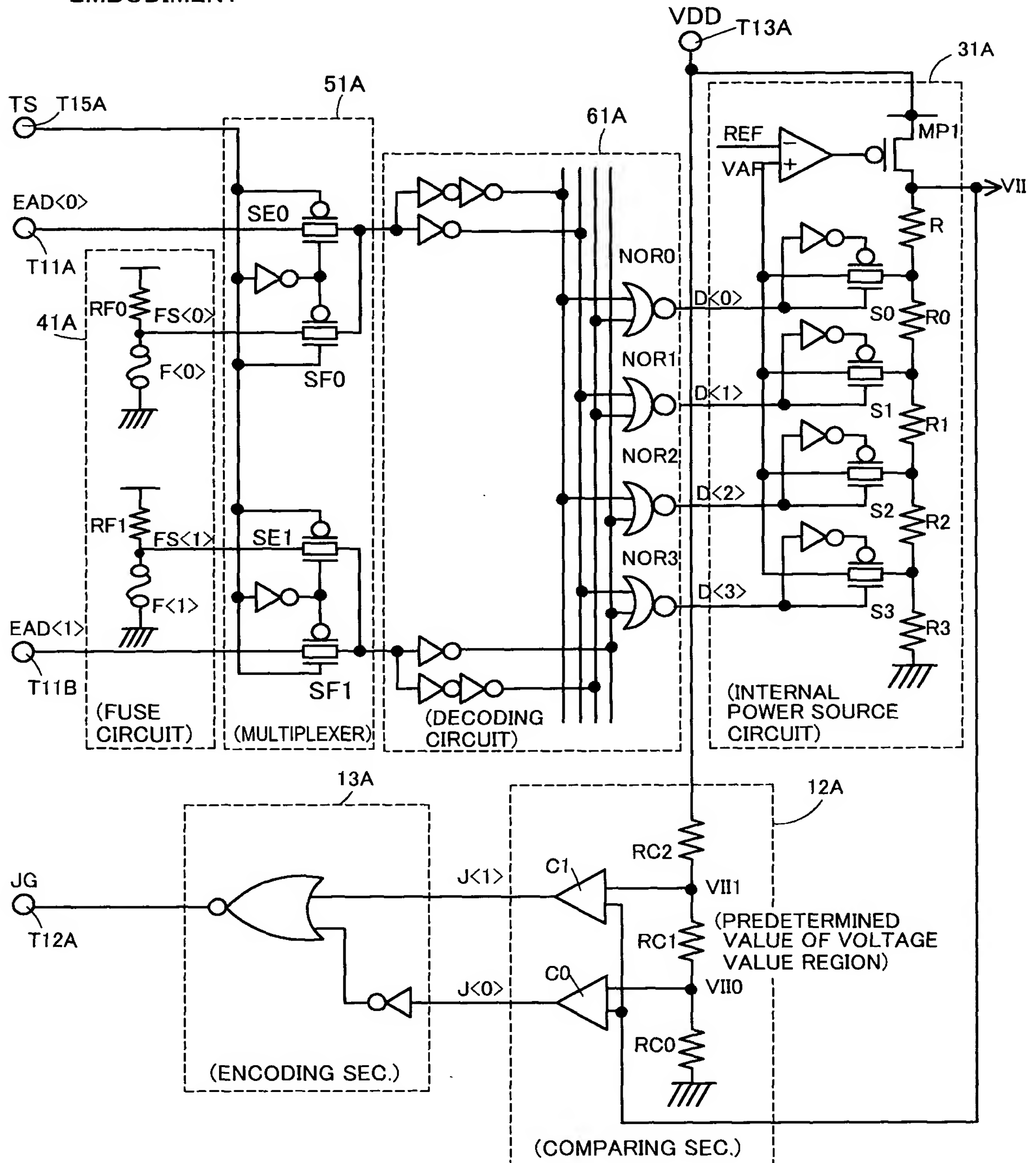


FIG. 5

OPERATIONAL WAVEFORM OF SPECIFIC EXAMPLE DIRECTED TO
FIRST EMBODIMENT

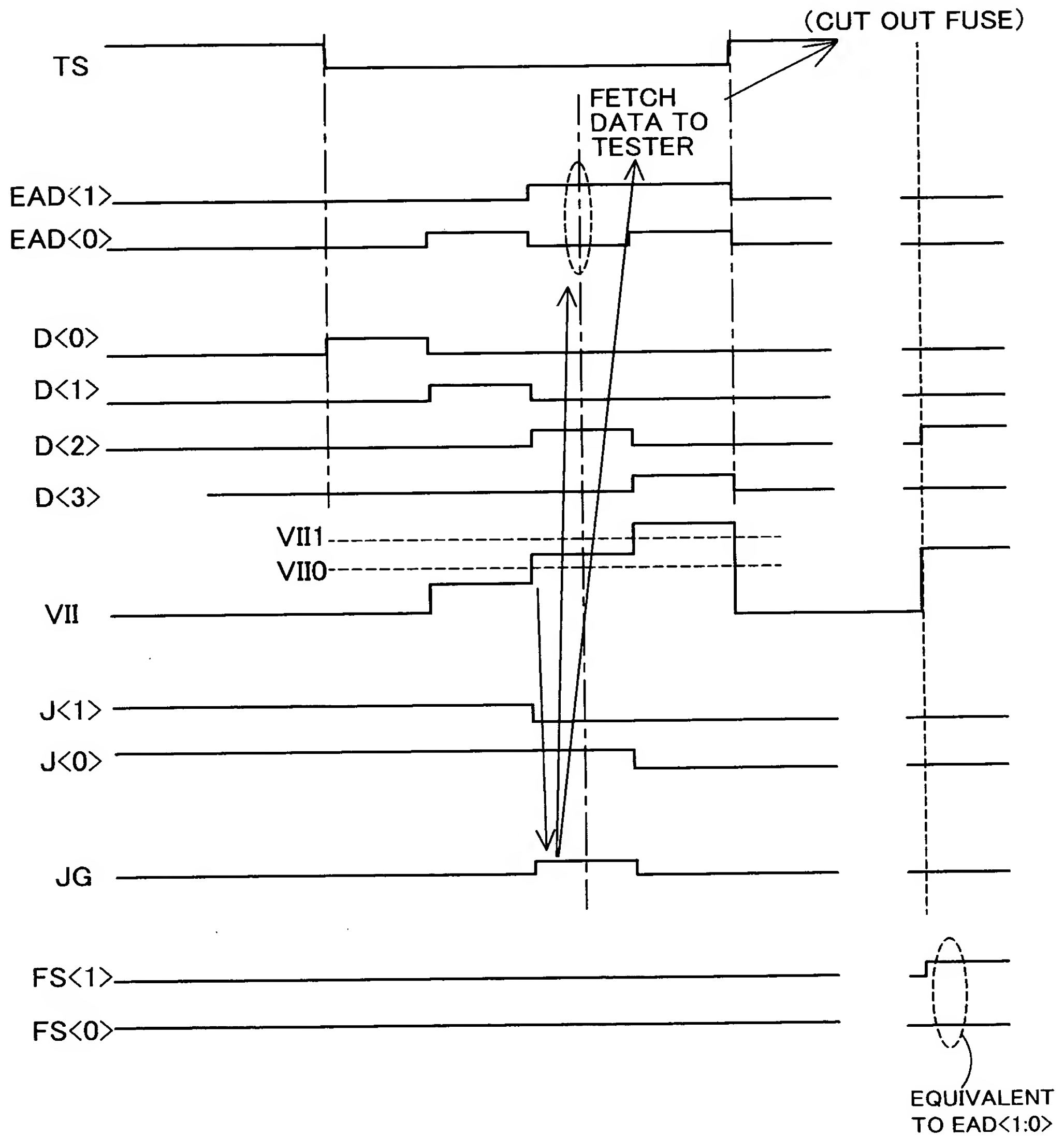


FIG. 6

TRIMMING TEST FLOW OF FIRST EMBODIMENT

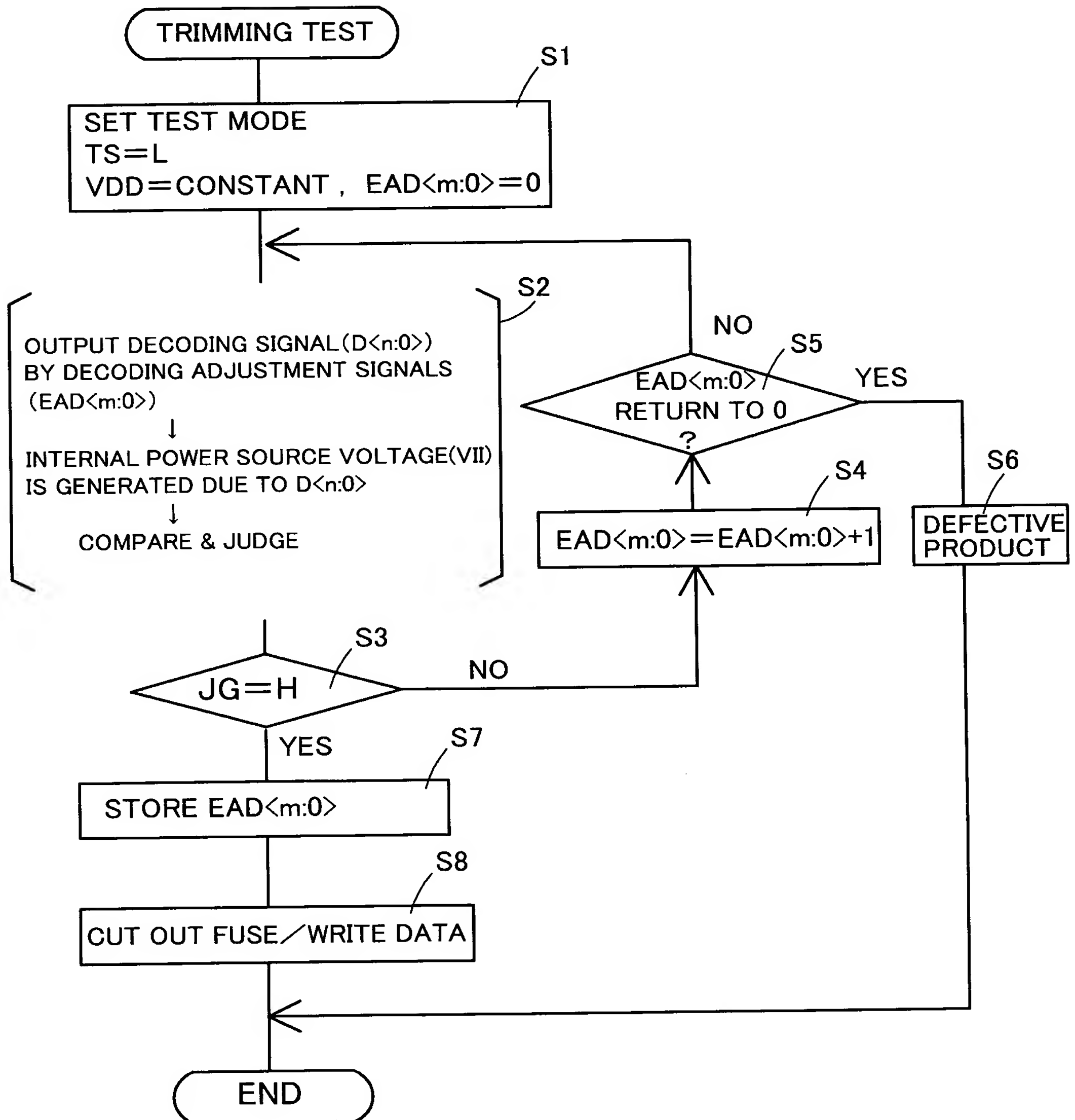
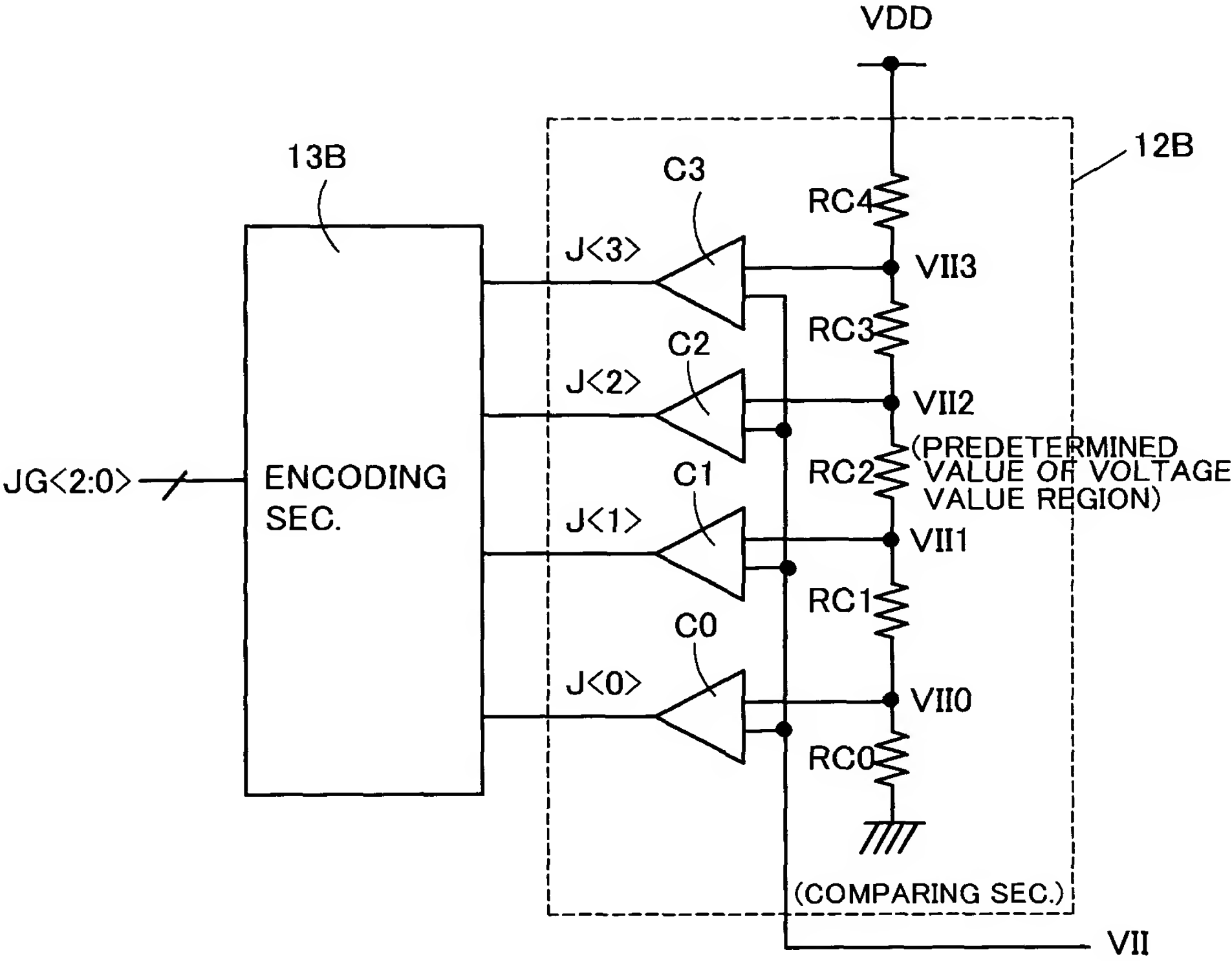


FIG. 7
CIRCUIT DIAGRAM OF VARIANT OF JUDGMENT SECTION



(ENCODING TABLE)

| J<3> | J<2> | J<1> | J<0> | JG<2> | JG<1> | JG<0> |
|------|------|------|------|-------|-------|-------|
| 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 0 | 0 | 0 | 1 | 0 | 0 | 1 |
| 0 | 0 | 1 | 1 | 0 | 1 | 0 |
| 0 | 1 | 1 | 1 | 0 | 1 | 1 |
| 1 | 1 | 1 | 1 | 1 | 0 | 0 |

← (PREDETERMINED
VALUE)

FIG. 8 CIRCUIT BLOCK DIAGRAM OF SECOND EMBODIMENT

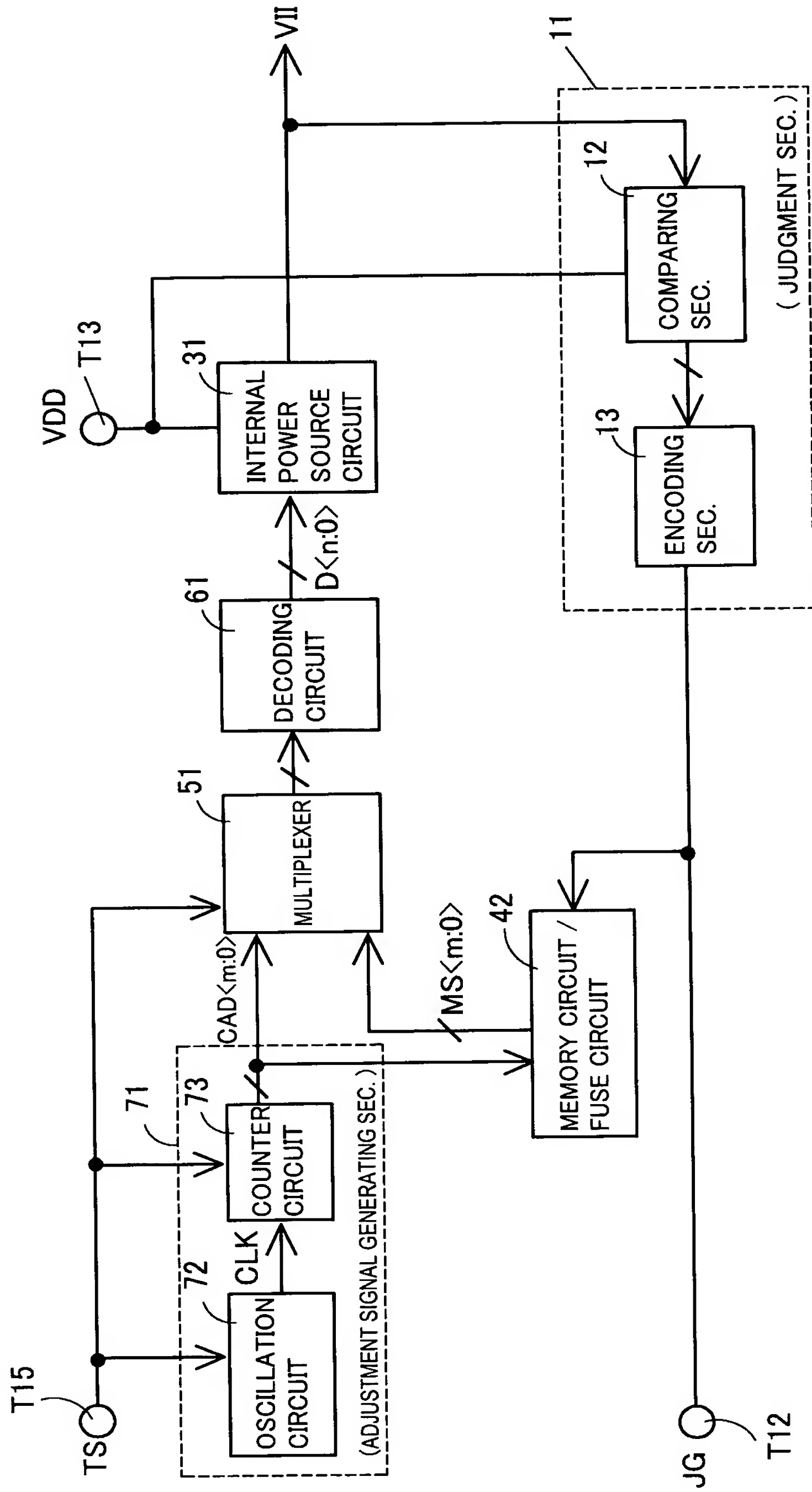


FIG. 9 CIRCUIT DIAGRAM OF SPECIFIC EXAMPLE DIRECTED TO SECOND EMBODIMENT

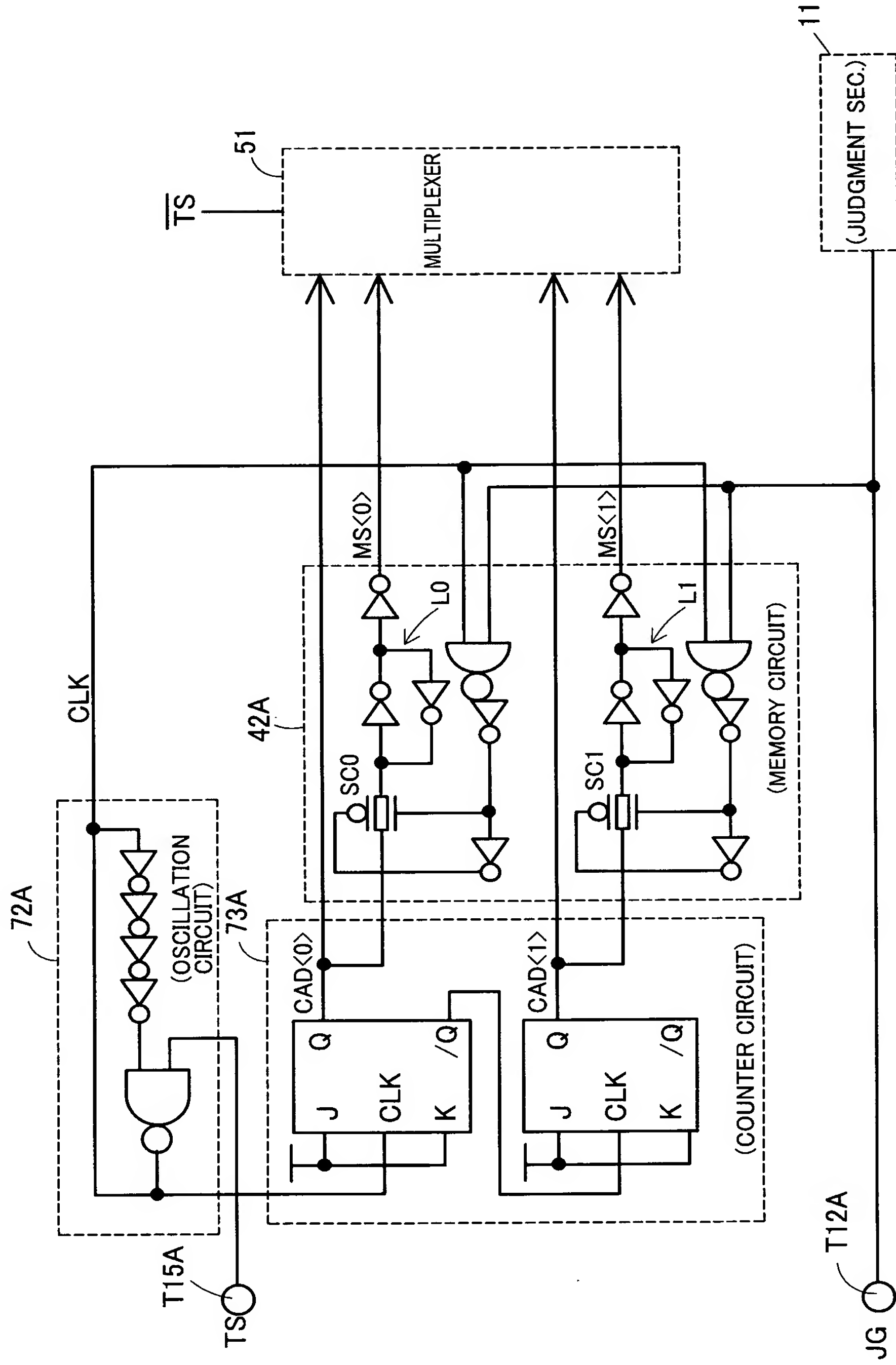


FIG. 10

OPERATIONAL WAVEFORM OF SPECIFIC EXAMPLE DIRECTED TO SECOND EMBODIMENT

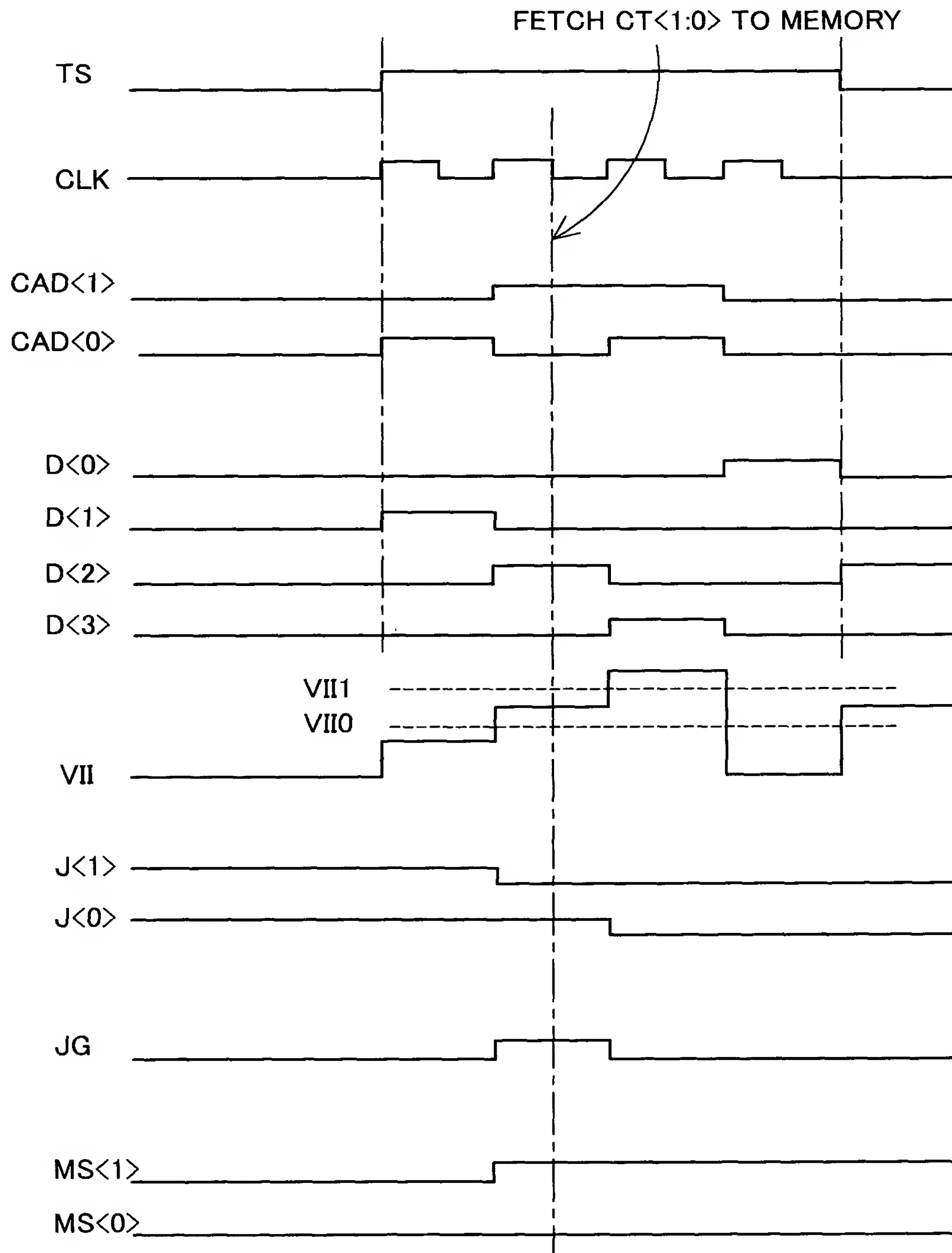


FIG. 11
TRIMMING TEST FLOW OF SECOND EMBODIMENT

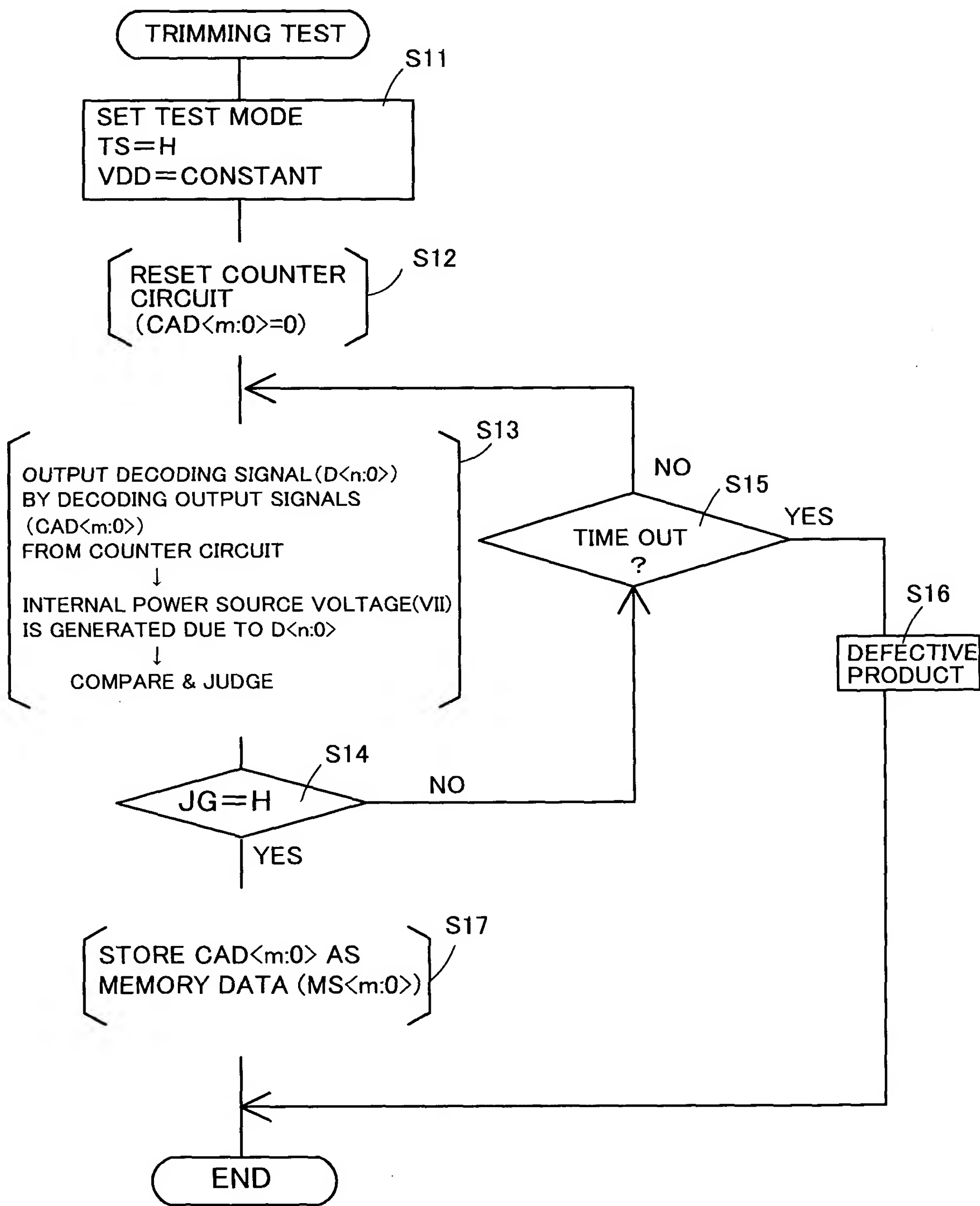


FIG. 12 CIRCUIT BLOCK DIAGRAM OF THIRD EMBODIMENT

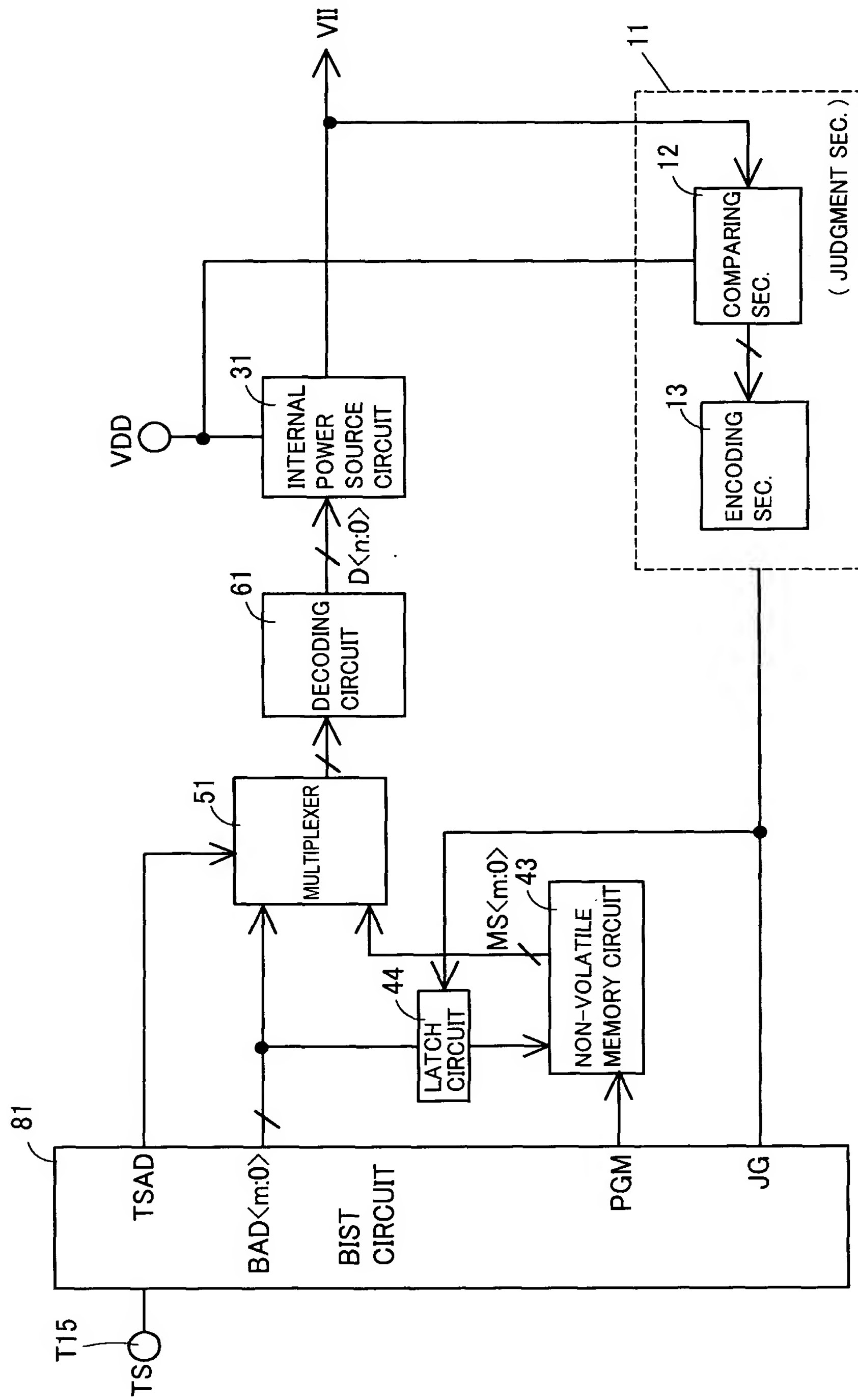


FIG. 14 CIRCUIT DIAGRAM OF FIRST SPECIFIC EXAMPLE DIRECTED TO FOURTH EMBODIMENT

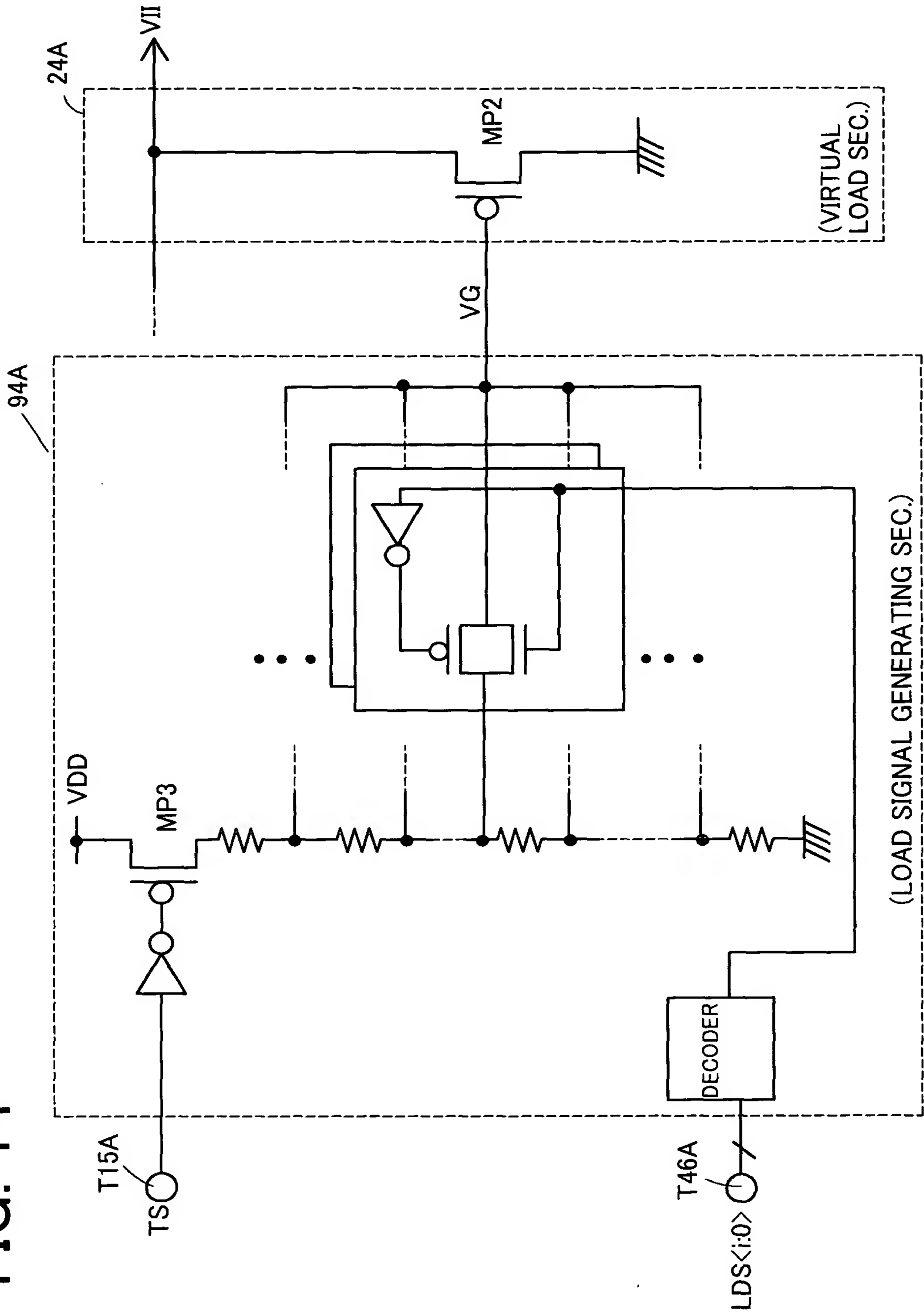


FIG. 15

CIRCUIT DIAGRAM OF SECOND SPECIFIC EXAMPLE DIRECTED TO FOURTH EMBODIMENT

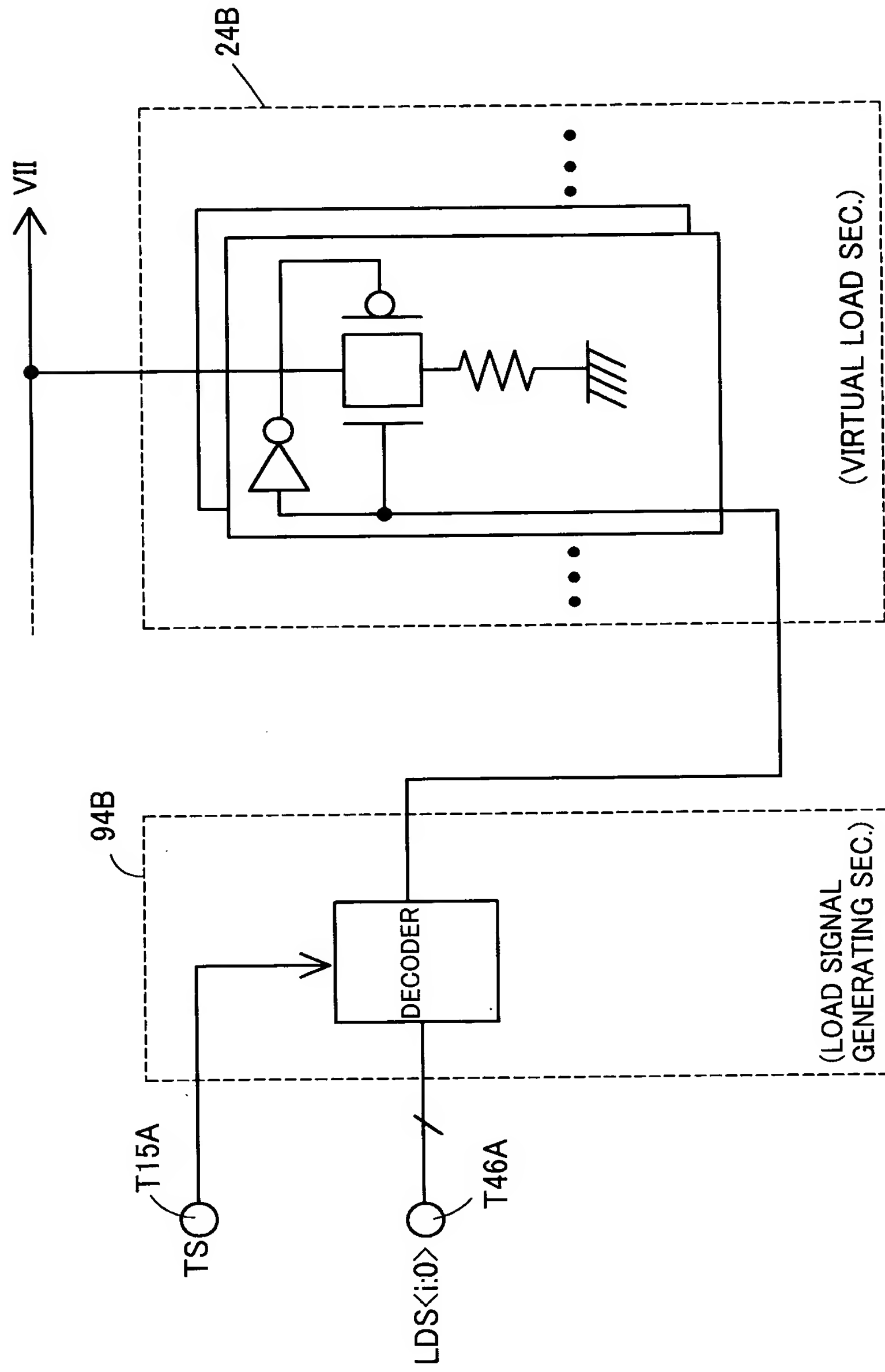


FIG. 16

CASE OF MULTIPLE JUDGMENT

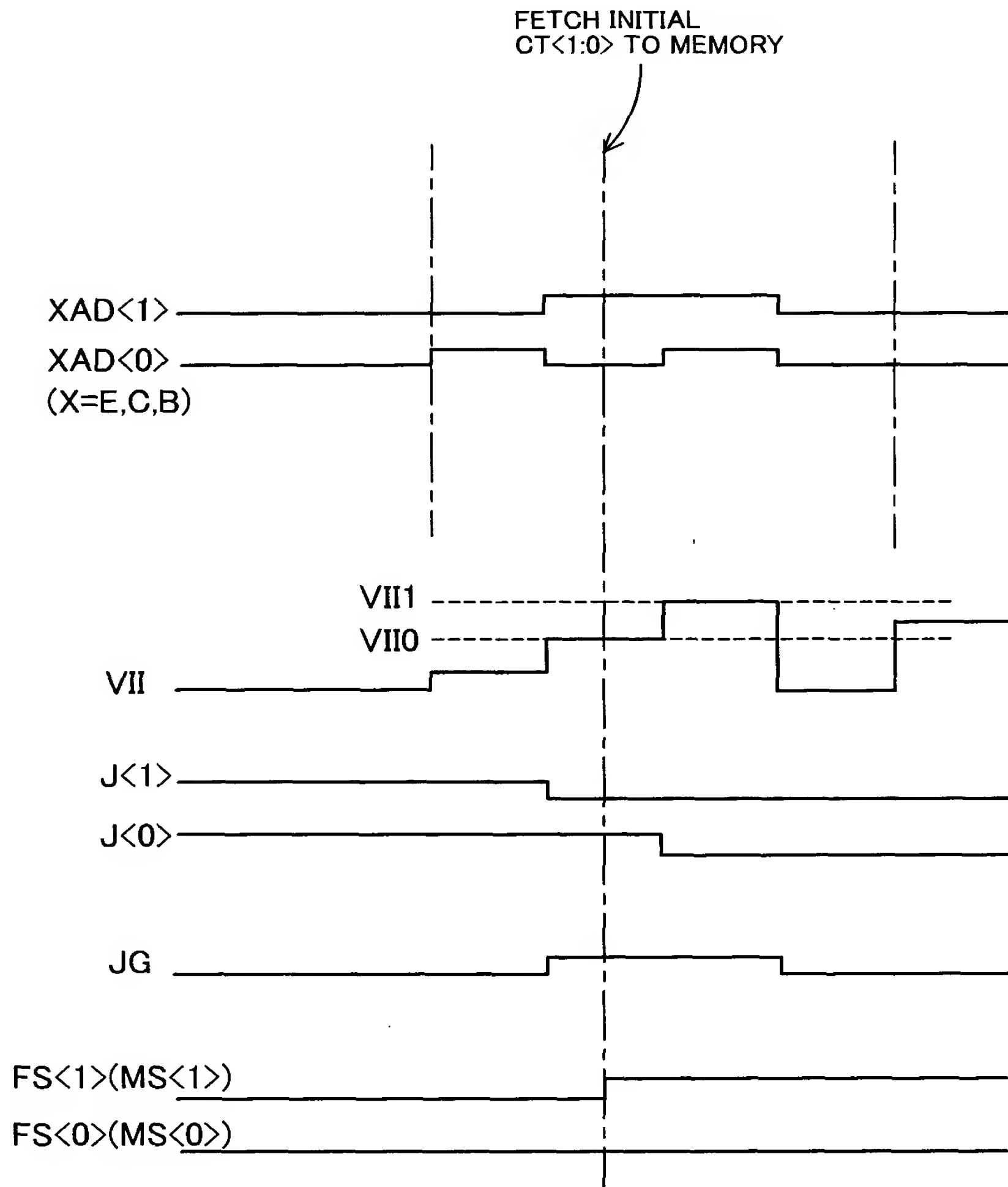


FIG. 17

PRIOR ART

CIRCUIT DIAGRAM OF PRIOR ART

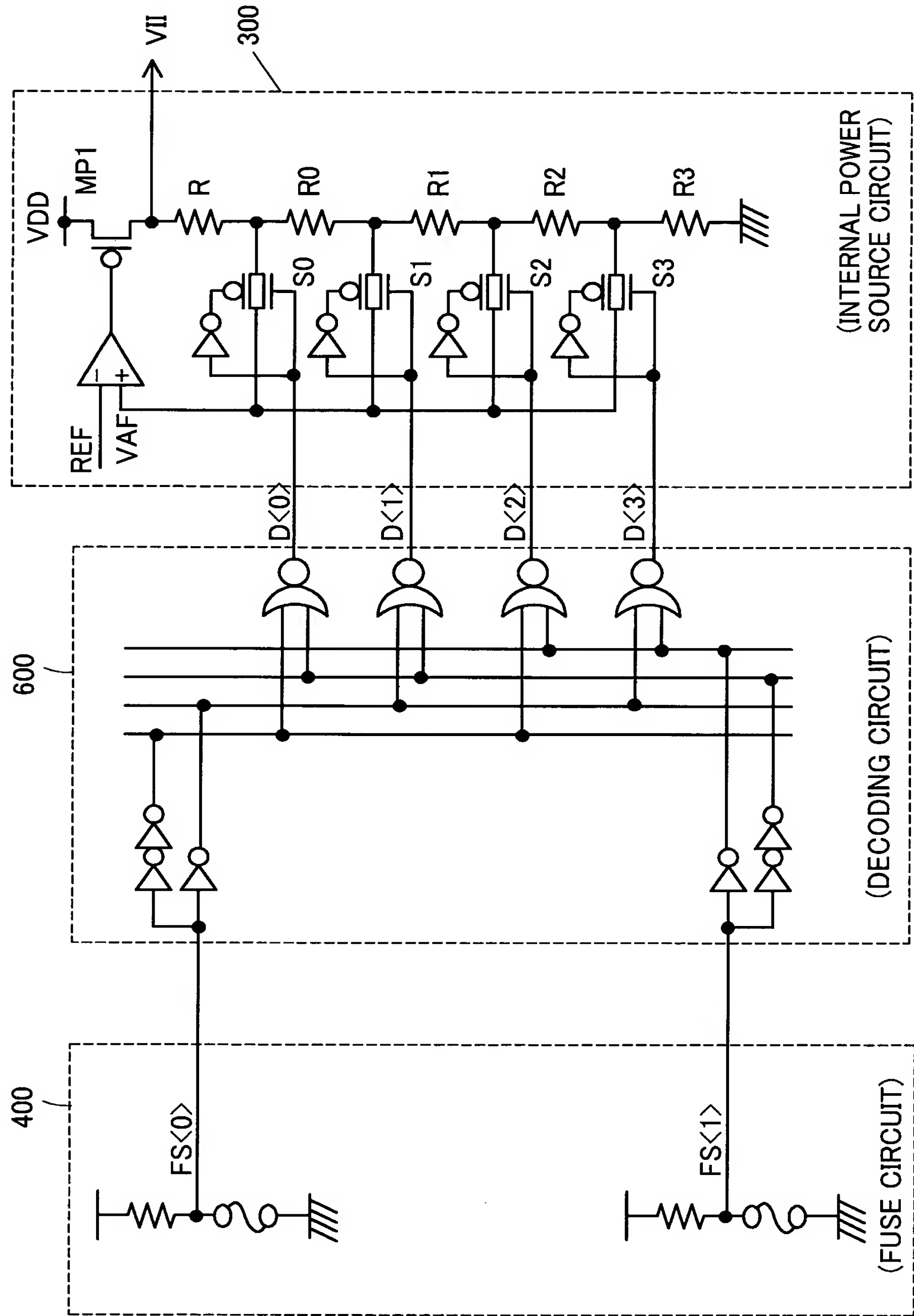


FIG. 18 PRIOR ART

CONVENTIONAL TRIMMING TEST FLOW

